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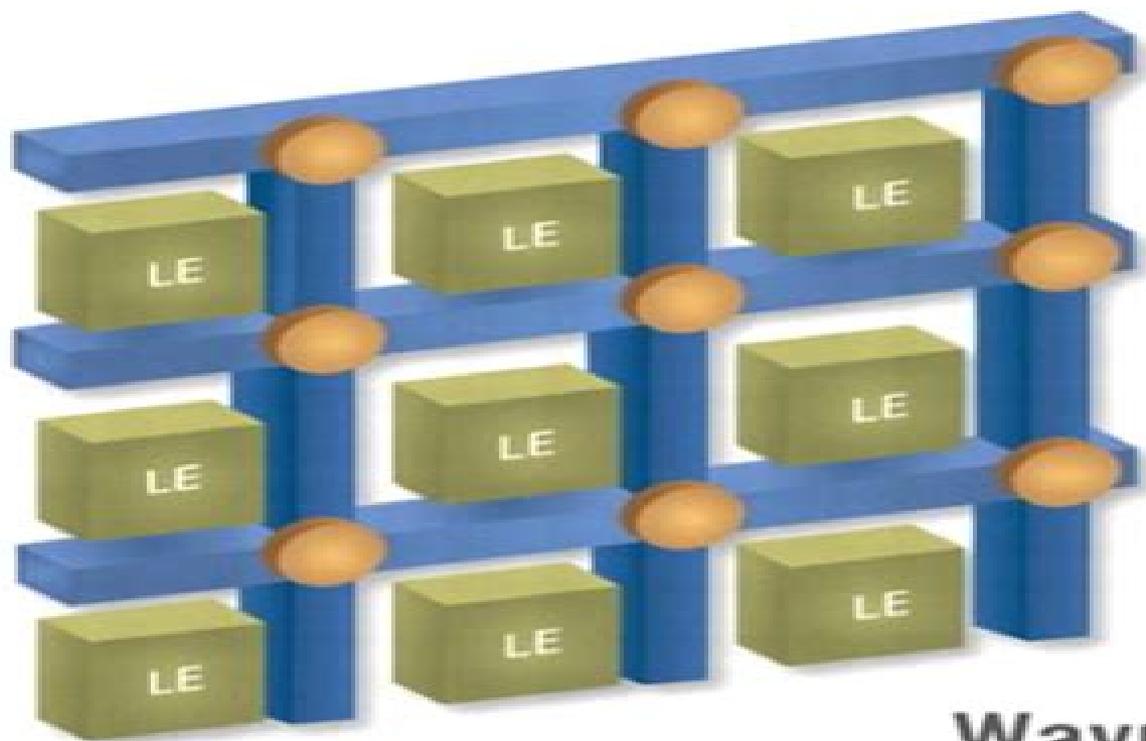
Wayne Wolf

Fpga Based System Design

Ming-Bo Lin

Fpga Based System Design:

    FPGA-Based System Design Wayne Wolf,2004-06-15 Digital designs once built in custom silicon are increasingly implemented in field programmable gate arrays FPGAs Effective FPGA system design requires a strong understanding of VLSI issues and constraints and an understanding of the latest FPGA specific techniques In this book Princeton University s Wayne Wolf covers everything FPGA designers need to know about all these topics both the how and the why Wolf begins by introducing the essentials of VLSI fabrication circuits interconnects combinational and sequential logic design system architectures and more Next he demonstrates how to reflect this VLSI knowledge in a state of the art design methodology that leverages FPGA s most valuable characteristics while mitigating its limitations Coverage includes   FPGA-Based System Design Wolf,2004-09   *FPGA-Based System Design* Wayne Hendrix Wolf,2004 Everything FPGA designers need to know about FPGAs and VLSI Digital designs once built in custom silicon are increasingly implemented in field programmable gate arrays FPGAs Effective FPGA system design requires a strong understanding of VLSI issues and constraints and an understanding of the latest FPGA specific techniques In this book Princeton University s Wayne Wolf covers everything FPGA designers need to know about all these topics both the how and the why Wolf begins by introducing the essentials of VLSI fabrication circuits interconnects combinational and sequential logic design system architectures and more Next he demonstrates how to reflect this VLSI knowledge in a state of the art design methodology that leverages FPGA s most valuable characteristics while mitigating its limitations Coverage includes How VLSI characteristics affect FPGAs and FPGA based logic design How classical logic design techniques relate to FPGA based logic design Understanding FPGA fabrics the basic programmable structures of FPGAs Specifying and optimizing logic to address size speed and power consumption Verilog VHDL and software tools for optimizing logic and designs The structure of large digital systems including register transfer design methodology Building large scale platform and multi FPGA systems A start to finish DSP case study addressing a wide range of design problems PRENTICE HALL Professional Technical Reference Upper Saddle River NJ 07458 www phptr com ISBN 0 13 142461 0   **A Tutorial on Fpga-Based System Design Using Verilog Hdl** Ming-Bo Lin,2018-08-09 The contents of this book are designed on the basis of the problem based learning PBL approach and follow the paradigm design entry in both schematic and HDL verification as well as implementation Based on this paradigm we develop an incremental learn by doing method to help the student to build a sound understanding in both the design principles and the implementa tions of digital systems based on FPGA devices Features of this book include Lab projects are exercised with schematic entry first and then Verilog HDL entry Both functional and timing verification are performed in each entry method to ensure the resulting design can work properly in FPGA devices The incremental learn by doing method is applied to gradually introduce new concepts and hardware resources and increase the depth of lab projects The paradigm design entry in both schematic and HDL verification as well as implementation is employed to familiarize the reader with the

right concept and use of the HDL entry method Optional lab projects are provided for readers to make realistic tests on FPGA devices Extended lab projects to broaden the reader s background knowledge and ca pability This book can be used as the textbook for the following courses Digital Logic Design Practice Introduction to FPGA Based System Design Introduction to Digital System Practice and Introduction to Verilog HDL **Introduction to Embedded System Design Using Field Programmable Gate Arrays** Rahul Dubey,2008-11-23 Introduction to Embedded System Design Using Field Programmable Gate Arrays provides a starting point for the use of field programmable gate arrays in the design of embedded systems The text considers a hypothetical robot controller as an embedded application and weaves around it related concepts of FPGA based digital design The book details use of FPGA vis vis general purpose processor and microcontroller design using Verilog hardware description language digital design synthesis using Verilog and Xilinx SpartanTM 3 FPGA FPGA based embedded processors and peripherals overview of serial data communications and signal conditioning using FPGA FPGA based motor drive controllers and prototyping digital systems using FPGA The book is a good introductory text for FPGA based design for both students and digital systems designers Its end of chapter exercises and frequent use of example can be used for teaching or for self study **Digital System Design with FPGA: Implementation Using Verilog and VHDL** Cem Unsalan,Bora Tar,2017-07-14 Master FPGA digital system design and implementation with Verilog and VHDL This practical guide explores the development and deployment of FPGA based digital systems using the two most popular hardware description languages Verilog and VHDL Written by a pair of digital circuit design experts the book offers a solid grounding in FPGA principles practices and applications and provides an overview of more complex topics Important concepts are demonstrated through real world examples ready to run code and inexpensive start to finish projects for both the Basys and Arty boards Digital System Design with FPGA Implementation Using Verilog and VHDL covers Field programmable gate array fundamentals Basys and Arty FPGA boards The Vivado design suite Verilog and VHDL Data types and operators Combinational circuits and circuit blocks Data storage elements and sequential circuits Soft core microcontroller and digital interfacing Advanced FPGA applications The future of FPGA **FPGA Design** Philip Simpson,2010-07-23 In August of 2006 an engineering VP from one of Altera s customers approached Misha Burich VP of Engineering at Altera asking for help in reliably being able to predict the cost schedule and quality of system designs reliant on FPGA designs At this time I was responsible for defining the design flow requirements for the Altera design software and was tasked with investigating this further As I worked with the customer to understand what worked and what did not work reliably in their FPGA design process I noted that this problem was not unique to this one customer The characteristics of the problem are shared by many Corporations that implement designs in FPGAs The Corporation has many design teams at different locations and the success of the FPGA projects vary between the teams There is a wide range of design experience across the teams There is no working process for sharing design blocks between engineering teams As I analyzed the data that I had received from

hundreds of customer visits in the past I noticed that design reuse among engineering teams was a challenge I also noticed that many of the design teams at the same Companies and even within the same design team used different design methodologies Altera had recently solved this problem as part of its own FPGA design software and IP development process

FPGA -Based Systems Design and Practice Ming-Bo Lin,2018-07-30 With the advance of semiconductor and communication industry the use of system on chip SoC has become an essential technique to reduce product costs The development of a good understanding of the key stages of the hardware description language HDL design flow based on cell based libraries or field programmable gate array FPGA devices becomes essential This book addresses the needs for such a topic based on Verilog HDL and FPGAs The most important features of this book include HDL based design has become an essential technique for modern digital systems This book focuses on developing verifying and synthesizing designs of practical digital systems using the most widely used hardware description Language Verilog HDL and FPGAs The main features of this book include Explaining how to perform synthesis and verification to achieve optimized synthesis results and compiler times Illustrating the entire design and verification flow using an FPGA case study Emphasizing design implementation trade off options with coverage of ASICs and FPGAs Providing plentiful worked examples and review questions in each section for readers to test their understanding of the related topics Giving readers deeper understanding with plentiful review questions in each section and end of chapter problems Incorporating many case studies to help the reader grasp the essentials of practical digital systems to be designed using Verilog HDL and FPGAs Highlighting Verilog HDL syntax throughout the book to facilitate readers to refer the desired syntax as they need Printing all keywords in boldface throughout the book to emphasize the language structures and improve the readability of Verilog HDL modules This book is the ideal textbook for the following courses Digital System Design FPGA System Designs and Practices Advanced Digital Systems Design and the like In addition it can be used as a self studying or professional reference book in this field

A Tutorial on Fpga-Based System Design Using Verilog Hdl Ming-Bo Lin,2018-08-17 The contents of this book are designed on the basis of the problem based learning PBL approach and follow the paradigm design entry in both schematic and HDL verification as well as implementation Based on this paradigm we develop an incremental learn by doing method to help the student to build a sound understanding in both the design principles and the implementa tions of digital systems based on FPGA devices Features of this book include Lab projects are exercised with schematic entry first and then Verilog HDL entry Both functional and timing verification are performed in each entry method to ensure the resulting design can work properly in FPGA devices The incremental learn by doing method is applied to gradually introduce new concepts and hardware resources and increase the depth of lab projects The paradigm design entry in both schematic and HDL verification as well as implementation is employed to familiarize the reader with the right concept and use of the HDL entry method Optional lab projects are provided for readers to make realistic tests on FPGA devices Extended lab projects to broaden the

reader s background knowledge and ca pability This book can be used as the textbook for the following courses Digital Logic Design Practice Introduction to FPGA Based System Design Introduction to Digital System Practice and Introduction to Verilog HDL   FPGA Design Philip Andrew Simpson,2015-05-19 This book describes best practices for successful FPGA design It is the result of the author s meetings with hundreds of customers on the challenges facing each of their FPGA design teams By gaining an understanding into their design environments processes what works and what does not work key areas of concern in implementing system designs have been identified and a recommended design methodology to overcome these challenges has been developed This book s content has a strong focus on design teams that are spread across sites The goal being to increase the productivity of FPGA design teams by establishing a common methodology across design teams enabling the exchange of design blocks across teams Coverage includes the complete FPGA design flow from the basics to advanced techniques This new edition has been enhanced to include new sections on System modeling embedded design and high level design The original sections on Design Environment RTL design and timing closure have all been expanded to include more up to date techniques as well as providing more extensive scripts and RTL code that can be reused by readers Presents complete field tested methodology for FPGA design focused on reuse across design teams Offers best practices for FPGA timing closure in system debug and board design Details techniques to resolve common pitfalls in designing with FPGAs   Cyber Physical Systems. Design, Modeling, and Evaluation Roger Chamberlain,Walid Taha,Martin Törngren,2019-04-12 This book constitutes the proceedings of the 7th International Workshop on Design Modeling and Evaluation of Cyber Physical Systems CyPhy2017 held in conjunction with ESWeek 2017 in Seoul South Korea in October 2017 The 10 papers presented together with 1 extended and 1 invited abstracts in this volume were carefully reviewed and selected from 16 submissions The conference presents a wide range of domains including robotics smart homes vehicles and buildings medical implants and future generation sensor networks   **A Tutorial on Fpga-Based System Design Using Verilog Hdl** Ming-Bo Lin,2018-08-17 The contents of this book are designed on the basis of the problem based learning PBL approach and follow the paradigm design entry in both schematic and HDL verification as well as implementation Based on this paradigm we develop an incremental learn by doing method to help the student to build a sound understanding in both the design principles and the implementations of digital systems based on FPGA devices Features of this book include Lab projects are exercised with schematic entry first and then Verilog HDL entry Both functional and timing verification are performed in each entry method to ensure the resulting design can work properly in FPGA devices The incremental learn by doing method is applied to gradually introduce new concepts and hardware resources and increase the depth of lab projects The paradigm design entry in both schematic and HDL verification as well as implementation is employed to familiarize the reader with the right concept and use of the HDL entry method Optional lab projects are provided for readers to make realistic tests on FPGA devices Extended lab projects to broaden the reader s background knowledge and capability This

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Electronic Design Automation for IC System Design, Verification, and Testing Luciano Lavagno,Igor L. Markov,Grant Martin,Louis K. Scheffer,2017-12-19 The first of two volumes in the Electronic Design Automation for Integrated Circuits Handbook Second Edition Electronic Design Automation for IC System Design Verification and Testing thoroughly examines system level design microarchitectural design logic verification and testing Chapters contributed by leading experts authoritatively discuss processor modeling and design tools using performance metrics to select microprocessor cores for integrated circuit IC designs design and verification languages digital simulation hardware acceleration and emulation and much more New to This Edition Major updates appearing in the initial phases of the design flow where the level of abstraction keeps rising to support more functionality with lower non recurring engineering NRE costs Significant revisions reflected in the final phases of the design flow where the complexity due to smaller and smaller geometries is compounded by the slow progress of shorter wavelength lithography New coverage of cutting edge applications and approaches realized in the decade since publication of the previous edition these are illustrated by new chapters on high level synthesis system on chip SoC block based design and back annotating system level models Offering improved depth and modernity Electronic Design Automation for IC System Design Verification and Testing provides a valuable state of the art reference for electronic design automation EDA students researchers and professionals **A Tutorial on Fpga-Based System Design Using Verilog Hdl** Ming-Bo Lin,2018-08-17 The contents of this book are designed on the basis of the problem based learning PBL approach and follow the paradigm design entry in both schematic

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customers approached Misha Burich VP of Engineering at Altera asking for help in reliably being able to predict the cost schedule and quality of system designs reliant on FPGA designs At this time I was responsible for defining the design flow requirements for the Altera design software and was tasked with investigating this further As I worked with the customer to understand what worked and what did not work reliably in their FPGA design process I noted that this problem was not unique to this one customer The characteristics of the problem are shared by many Corporations that implement designs in FPGAs The Corporation has many design teams at different locations and the success of the FPGA projects vary between the teams There is a wide range of design experience across the teams There is no working process for sharing design blocks between engineering teams As I analyzed the data that I had received from hundreds of customer visits in the past I noticed that design reuse among engineering teams was a challenge I also noticed that many of the design teams at the same Companies and even within the same design team used different design methodologies Altera had recently solved this problem as part of its own FPGA design software and IP development process **Embedded Core Design with FPGAs** Zainalabedin Navabi,2006-09-13 A Complete Toolkit for Designing Embedded Cores and Utilizing Those Cores in an Embedded System A landmark guide in digital system design Embedded Core Design with FPGAs equips today s computer engineers with everything they need to design embedded cores and apply those cores in a state of the art embedded system This practical resource brings together logic design computer architecture Verilog FPGAs Hardware Software design and SoCs explaining how engineers can draw on their computer engineering background to achieve cutting edge embedded designs Renowned design expert and educator Zainalabedin Navabi first covers the basics of logic design RT Level Verilog computer architectures and the architecture of modern field programmable devices He then explores the design of utility cores that are used for high level core based designs with specific focus on existing Altera cores Finally he describes higher end design methodologies including design of hardware software systems CPU configurations embedded systems and the utilization of various Altera Nios II processors Embedded Core Design with FPGAs features A full array of design aids including Verilog FPLD structures design and programming environments and software and hardware tools The latest embedded system design techniques including use of high level integrated environments SOPC development tools utilizing existing processor cores and developing your own customized processor A clear focus on utilizing Altera s new DE series and UP3 development boards and design software including SOPC Builder and IDE software design environment Master Every Aspect of Embedded Core Design High Level Hardware Software Design Concepts High Level System Design Methodology RT Level Logic Design RT Level Verilog Computer Hardware and Software Programming Languages FPGA Architecture and Utilization FPGA Based Design of Embedded Cores Implementation of Basic Interface Components Configurable Cores Custom Cores CPU Cores Core Based System Design Using Development Boards for Prototyping System Design with Processor Cores Design with a Customer Embedded CPU Embedded Core DSP Application Embedded Microcontroller with

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web according to the patient s clinical findings the most probable diagnosis is cushing s syndrome due to an adrenal tumor the patient s past medical history physical exam and lab results are consistent with this diagnosis if further imaging or workup is indicated a plasma dheas

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web he first focused on the thyroid since that is the gland often associated with metabolism list tim s symptoms and identify the organ system or specific organ associated with those symptoms your may need to reference other materials to

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web jan 1 2013 after pituitary imaging studies he should be treated with a dopamine agonist transsphenoidal surgery or both and subsequently with parathyroid surgery see chapters 20 and 51 23 a 52 year old woman complains of a 1 year

history of progressive fatigue puffy eyes dry skin and mild weight gain

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web endocrinology case studies endocrinology is the branch of medicine dealing with the conditions of endocrine glands and their secreted hormones there are several endocrine glands in the human body namely the pituitary hypothalamus thyroid parathyroid thymus pancreas adrenal glands testis and ovary source doi

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web endocrine case study genevieve is a 45 year old woman with three children who works part time and has recently returned to nursing school she has sought care from her primary care provider

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web the disorder is acromegaly the hormone of hypersecretion is gh autumn age 30 has had noticeable weight gain resulting in purple striae stretch marks along the abdomen she has increased deposits of adipose tissue in the face moon face the shoulders buffalo hump neck and trunk

endocrine case studies student endocrine case studies use - Oct 19 2023

web answers to renal review questions use your knowledge of the endocrine system plus your internet search skills to answer the questions for each of these cases a 30 year old female demonstrated a subtle onset of the following symptoms dull facial expression droopy eyelids puffiness of the face and periorbital swelling sparse dry hair dry

endocrine case study with answers docx course hero - Feb 28 2022

web view endocrine case study with answers docx from ns 40 at santa monica college case study chapter 52 assessment and management of patients with endocrine disorders 1 don smart 55 years of age

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web read the following information and enter your answers in the spaces provided you are expected to include supportive rationale published within the last five years with your responses additionally you need to include national guidelines for treatment options

a p2 lab practical 1 endocrine case studies flashcards - Jan 10 2023

web case study 1 in this endocrine condition they thyroid gland under produces certain hormones tsh t3 t4 and since the thyroid gland regulates how the body uses energy if there isn t enough hormones for the body to use bodily functions begin to slow down

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web endocrine case studies a 19 year old bctc student was admitted to the uk medical center because of extreme fatigue and weight loss for the previous year he had tired easily had lost 64 pounds in the last 2 years his present height is

endocrine case studies flashcards quizlet - Sep 06 2022

web study with quizlet and memorize flashcards containing terms like a 30 year old female who is 32 weeks pregnant begins to ex perience tremors heat intolerance and irritability along with some fatigue tachycardia hypertension and lower abdomi nal pain

12 1 case study hormones and health biology libretexts - Apr 13 2023

web chapter overview endocrine system in this chapter you will learn about the endocrine system a system of glands that secrete hormones that regulate many of the body s functions specifically you will learn about the glands that make up the endocrine system and how hormones act as chemical messengers in the body

essay on cheer captain 603 words internet public library - Jun 19 2023

web becoming cheer captain would give me the opportunity to be a good image to the squad showcase my leadership abilities and motivate my squad to spread spirit all throughout the school

cheer captain essay 726 words bartleby - Oct 11 2022

web 726 words 3 pages open document cheer captain essay 11 28 17 to be a cheer captain there are many responsibilities and priorities to be a captain you have to be strong and not afraid to try new things you need to have a great positive attitude the whole time and not let the drama start on the team

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web mom look i made the team i had waited my whole cheer career just for this moment throughout the years of being a wshs cheerleader my love for the team grew more and more each year my junior year of high school i tried out for what i thought i had always wanted cheer captain i worked for hours upon hours day and night

how to get named cheerleading captain 10 steps with pictures - Jul 08 2022

web apr 24 2023   always stretch before doing any stunts it s important that you have stretched out your muscles because you don t want to pull a muscle 3 be loud and full of energy when you re shouting a cheer make sure to shout it loudly and proudly 4 get good grades 5 always smile

great cheer captain speeches 597 words bartleby - Apr 17 2023

web great cheer captain speeches cheerleading speeches many of the cheerleaders would agree it is a privilege and an honor to be a part of the throw a football speech want to know how to throw a perfect spiral well then get up and go outside a beautiful day cheer captain speech examples on

cheer captain speech stage gapinc com - Oct 31 2021

web cheer captain speech 5 5 speeches literary and social collects dickens s speeches from the early 1840s onward in these speeches we see him discussing his own work and various other topics with humility and grace these speeches offer the

reader insight into dickens the man the public figure the ordinary citizen they also provide

cheer captain speech - Jan 02 2022

web adventures of captain bonneville sep 01 2021 w irving s works the adventures of captain bonneville u s a in the rocky mountains and the far west the conquest of florida under hernando de soto oct 22 2020 speech of john m clayton of delaware in regard to captain s f du pont u s n in the senate of the united states march 11

great cheer captain speeches ppt slideshare - Aug 09 2022

web jul 4 2023   great cheer captain speeches i stood there in anticipation speech in hand facing the crowd then i ripped apart the speech but i had no worries about the paper after all i had already memorized the whole speech at that moment my goal was to inspire people with my speech

an open letter to my cheerleading team the odyssey online - Sep 10 2022

web jan 08 2016 george washington university to my cheerleading team i just want to begin by saying there is no other place that i would have spent my high school years than on the mat with all of you there is a bond like no other athletes that cheerleaders have for each other and it is so special i will never forget it

how to write a cheerleading captain essay sportsrec - Oct 23 2023

web jan 1 2011   a well organized concretely expressed essay will show the coach that you re the best fit for the job write your opening paragraph explain what cheerleading means to you and why you want to be captain of the squad describe your particular vision of what cheerleading means to you

cheer captain speech answerprime - Jun 07 2022

web may 28 2023   overall i want our cheerleading squad to reach their full potential if you choose me as captain the fans will be in good hands i want to be cheering captain because i want this team to be the best it can be being captain can be one of the most important aspects of cheerleading but with my hard work and determination i can take

cheer captain speeches 685 words cram - May 06 2022

web ever since i was six years old cheering on my local pop warner team i have dedicated an unwavering amount of time and dedication to my beloved sport whether it s staying at school until six o clock every day or spending my weekends making choreography for my team cheer affects my life inside and outside of school

2017 year 6 school captains speeches exford primary school - Feb 03 2022

web hi my name is antonio and i would like to run for school captain ever since i have heard about school captain i ve always wanted to become one was inspired by the previous school captains and i would like to achieve that same goal

qualities a cheerleading captain needs work chron com - Mar 16 2023

web aug 16 2021   it might mean leading stretching and conditioning taking over coaching at practice sometimes cheer

captain speeches leading cheers at games leading or delegating outside activities or

what are the responsibilities of a cheerleading captain - Feb 15 2023

web jan 30 2019   traits of a good cheerleading captain and co captain good communication skills you should be able to not only speak clearly but also be a very good listener responsibility and maturity you should be

cheer captain speech examples 320 words bartleby - Jul 20 2023

web cheer captain speech examples satisfactory essays 320 words 2 pages open document on the morning of saturday october 31st we had cheer practice ms perez had brought up the idea of captains for this year s cheer team all of us thought it was a good idea so she decided that we would vote today

cheer captain speech answerdata - Apr 05 2022

web jul 19 2021   i want to be cheer captain because i want to make this team the best it can be being captain can be one of the most important aspects of a cheer leading squad but with my hard work and determination i can take this squad far i am well prepared to take on the responsibility s that come my way

the 3 c s of being a captain applied sport psych - Jan 14 2023

web larry lauer phd and kevin bluemichigan state university major point captains embody 3 c s in leading their team caring courageous and consistent being named a team captain is quite the honor the position of captain is given to those athletes whom the rest of the team respect and trust to lead the team in the right direction

a speech on being a cheerleader 907 words bartleby - Mar 04 2022

web kaitlyn woodward zane state mr wilcoxon english 1500 cheerleading is more than just dressing up in a skirt and trying to get all the jocks to like you many valuable lessons for me have resulted during my time as a cheerleader cheer has opened my eyes to many different things over the years

cheerleading captain essay phdessay com - Dec 13 2022

web aug 17 2016   a good cheerleading captain should be a strong leader who is organized reliable and has a positive attitude they should also be a good communicator and be able to motivate and inspire their team lastly they should be a role model for their team setting a good example of hard work and dedication

being a cheerleader captains and officers varsity com - May 18 2023

web oct 8 2014   be cooperative think of yourself as a mediator not an instigator listen to your what your coaches and teammates have to say and try to remain flexible and collaborative be responsible this is the tough part part of your job is to keep the team in line positive energy is key you may need to be stern at times but try to keep your tone

cheerleading captain speech 325 words studymode - Sep 22 2023

web filter results good evening everyone i hope everyone is enjoying their night my name is keyona and i was the one of the 4

captains chosen for the 2011 montclair high school cheerleading squad during my 3 years on the squad i ve had so many memories good and bad that helped me grow as a person

why i want to be a cheer captain internet public library - Nov 12 2022

web cheerleading itself is a very special privilege that people all around the world get to have the opportunity to do making the cheer team is one of the most exciting times of one s life but becoming captain would be once in a lifetime opportunity that would be a

s pore football fans cheer incredible hulk lookalike at thailand - Dec 01 2021

web 15 hours ago   next up was the leader of the avengers captain america who looked like he had ventured a little ahead through the quantum realm image via jksg98 tiktok then came thor junior who looked pretty