

Error Control Coding

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graph TD; A[Error Control Coding] --> B[Linear Block Codes]; A --> C[Convolutional Codes]; B --> D[Hamming Codes]; B --> E[BCH codes]; B --> F[Cyclic codes]; B --> G[RS codes]; C --> H[Turbo codes]; C --> I[Trellis Diagram];
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Linear Block Codes

Hamming Codes

BCH codes

Cyclic codes

RS codes

Convolutional Codes

Turbo codes

Trellis Diagram

Error Control Coding For Computer Systems

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